IN THE CLAIMS

1. (Previously Amended) Apparatus for switching data from any of a plurality of inputs to any of a plurality of outputs, comprising:

apparatus for receiving a plurality of input bit packs organized in a combination of input data rails and time slots,

apparatus for selecting any ~~one~~ of the input bit packs from any ~~one~~ of the rails in any ~~one~~ of the time slots, and

apparatus for conveying said selected bit pack to any ~~an~~ output data position within a combination of output data rails and time slots.

2. (Original) Apparatus of claim 1, wherein each bit pack is one bit wide.

3. (Original) Apparatus of claim 1, wherein said apparatus for receiving, selecting, and conveying a plurality of bit packs is configured for selecting a plurality of input bit packs for output in a plurality of output data positions.

4. (Original) Apparatus of claim 1, wherein said apparatus for receiving, selecting, and conveying a plurality of bit packs is configured for selecting a single bit pack for output in a plurality of output positions.

5. (Previously Amended) Apparatus for switching data from any of N input positions arranged as T time slots on R rails to any of M output positions arranged as T2 time slots on R2 rails, comprising:

apparatus for receiving input data arranged as bit packs in T time slots on R rails,

apparatus for selecting data from any ~~one~~ of the R rails and latching the selected

data during a predetermined time slot to thereby select a bit pack of predetermined R and T values, and

apparatus for conveying said selected bit pack to any ~~an~~ output position of predetermined R2 and T2 values.

6. (Previously Amended) Apparatus for switching data from any of N input positions arranged as T time slots on R rails to any of M output positions arranged as T2 time slots on R2 rails, comprising:

M selection blocks, each configured to select a bit pack for a different one of the output positions, and each block including:

apparatus for receiving input data arranged as bit packs in T time slots on R rails,

apparatus for selecting data from any ~~one~~ of the R rails and latching the selected data during a predetermined time slot to thereby select a bit pack of predetermined R and T values, and

apparatus for conveying said selected bit pack to any ~~an~~ output position of predetermined T2 and R2 values.

7. (Original) Apparatus of claim 6 further comprising:

a T2 X R2 output bit map configured for receiving a selected bit pack in each location from a different one of the M selection blocks.

8. (Original) Apparatus of claim 7 further comprising:

a second T2 X R2 output bit map configured to be loaded in parallel from the first output bit map.

9. (Original) Apparatus of claim 8 further comprising:

apparatus configured to arrange input bit packs as an array of T time slots on R

rails and to convey output bit packs from the second T2 X R2 bit map on R2 rails in T2 time slots.

10. (Original) Apparatus of claim 9 wherein N = M = 768.

11. (Previously Amended) Apparatus for switching data from any of N input positions arranged as T time slots on R rails to any of M output positions arranged as T2 time slots on R2 rails, comprising:

R2 selection blocks, each configured to select a bit pack for a different one of the output positions, and each block including:

apparatus for receiving input data arranged as bit packs on N rails,

apparatus for selecting data from <u>any</u> ~~one~~ of the N rails, and

apparatus for conveying said selected bit pack to <u>any</u> ~~an~~ output position of predetermined T2 and R2 values.

12. (Original) Apparatus of claim 11 further comprising:

a T X R input bit map configured for receiving a selected bit pack in each location from a different one of the N space/time input positions.

13. (Original) Apparatus of claim 12 further comprising:

a second T X R input bit map configured to be loaded in parallel from the first input bit map and to convey N input bit packs to each of the R2 selection blocks and to hold the N input bit packs available to the R2 selection blocks during T2 time slots.

14. (Currently Amended) Apparatus of claim <u>11</u> ~~10~~ further comprising:

apparatus configured to arrange input bit packs as an array of T time slots on R

rails and to convey output bit packs from the second T2 X R2 bit map on R2 rails in T2 time slots.

15. (Original) Apparatus of claim 14 wherein N = M =768.

16. (Original) A method of switching data from any of N input positions arranged as T time slots on R rails to any of M output positions arranged as T2 time slots on R2 rails, comprising the steps of :

(a) in each of R2 selection blocks, selecting a bit pack for a different one of the output positions, and

(b) conveying each of the bit packs selected in step (a) to the associated one of the output positions.

17. (Original) The method of claim 16 wherein step (a) comprises the further step of :

(c) receiving input data arranged as bit packs on N rails.

18. (Previously Amended) The method of claim 17 wherein step (a) comprises the further step of:

(d) selecting a bit pack from <u>any</u> ~~one~~ of the N rails.

19. (Original) The method of claim 18 wherein step a comprises the further step of:

(e) conveying said selected bit pack to an output position of predetermined T2 and R2 values.

20. (Original) A method of switching data from any of N input positions arranged as T time slots on R rails to any of M output positions arranged as T2 time slots on R2 rails, comprising the steps of :

(a) in each of M selection blocks, selecting a bit pack for a different one of the output positions, and

(b) conveying each of the bit packs selected in step (a) to the associated one of the output positions.

21. (Previously Amended) The method of claim 20 wherein step (a) further comprises the steps of:

(c) receiving input data arranged as bit packs in T time slots on R rails, and

(d) selecting data from any ~~one~~ of the R rails and latching the selected data during a predetermined time slot to thereby select a bit pack of predetermined R and T values.

22. (Currently Amended) The method of claim 21 wherein step (b) further comprises the step of:

(e) conveying said selected bit pack to any ~~an~~ output position of predetermined T2 and R2 values.